

Fig. 1

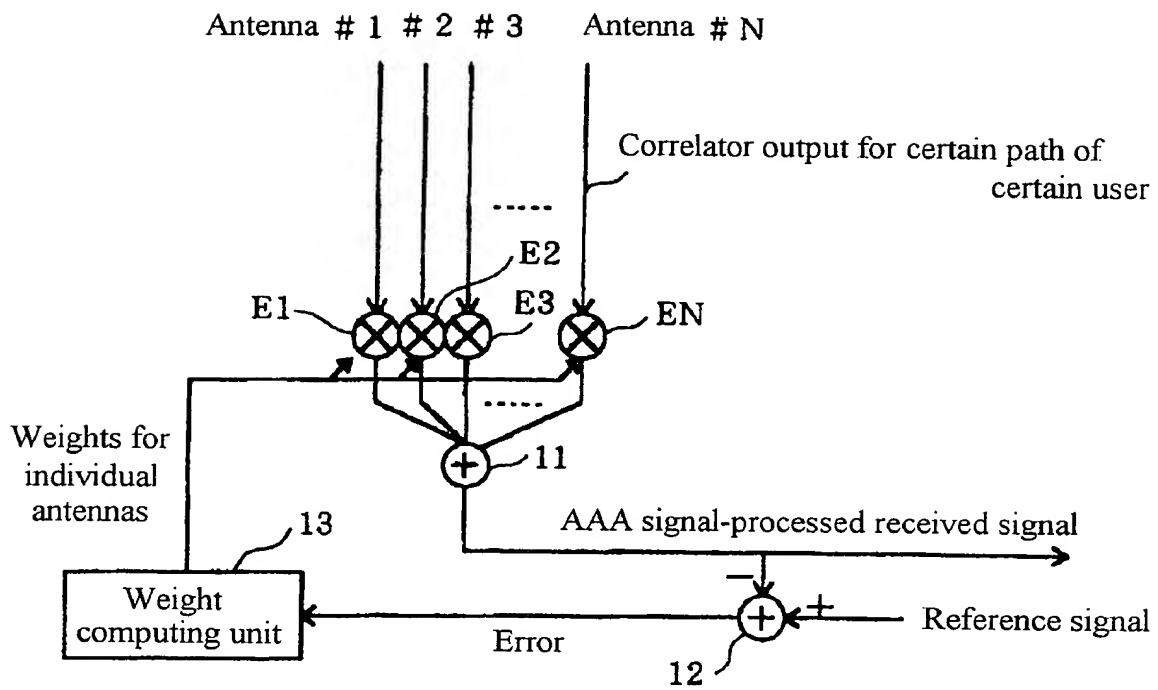
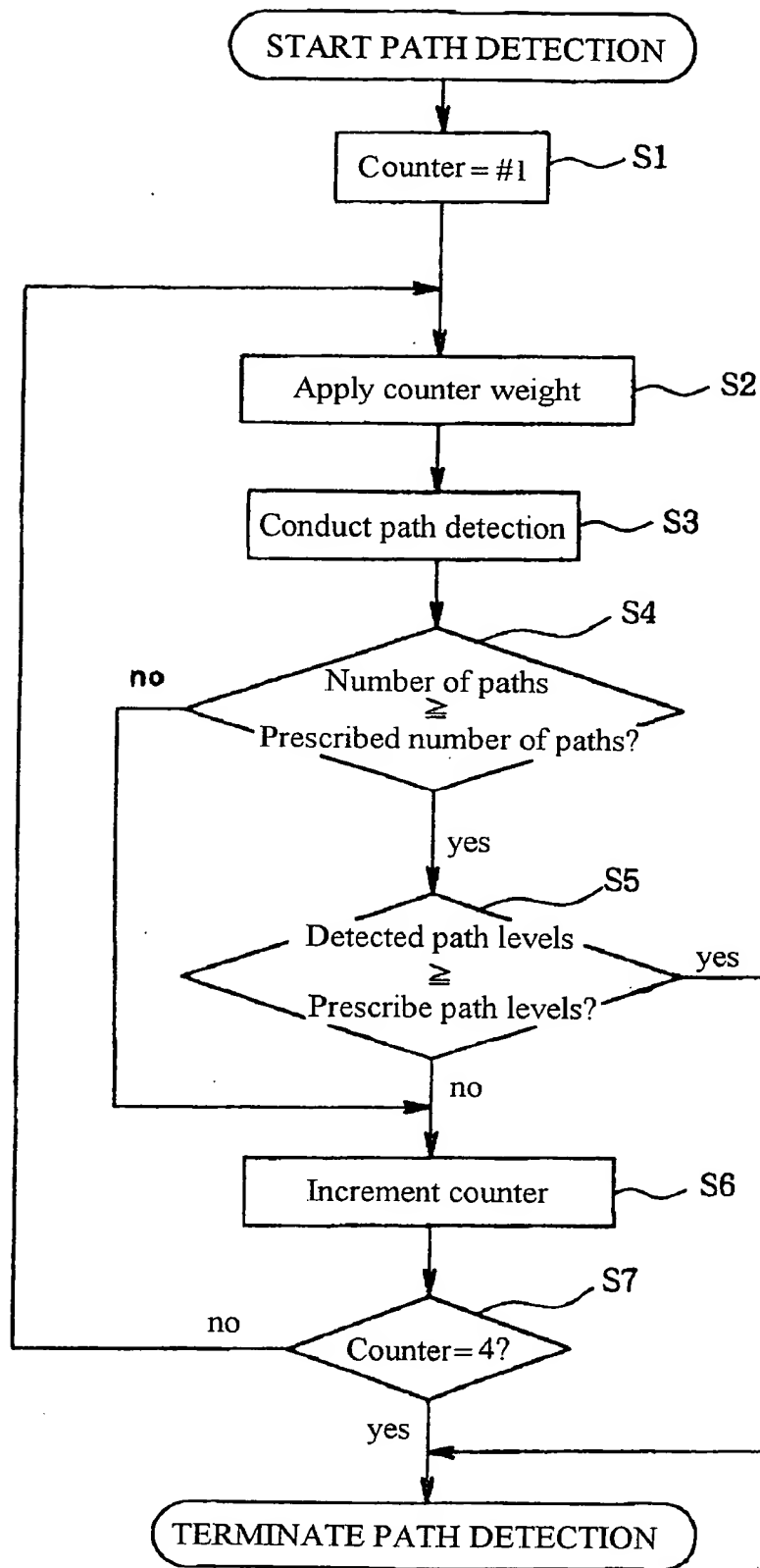


Fig. 2

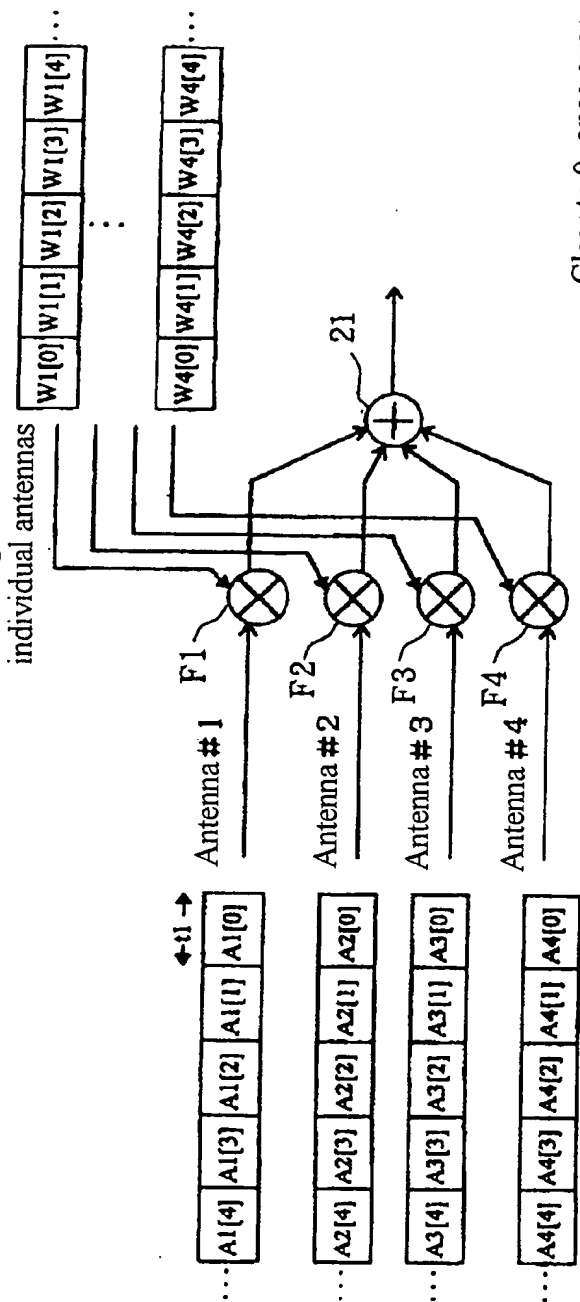
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#1 : Leading path weight
#2 : Highest path weight
#3 : Average path weight
#4 : Lowest path weight

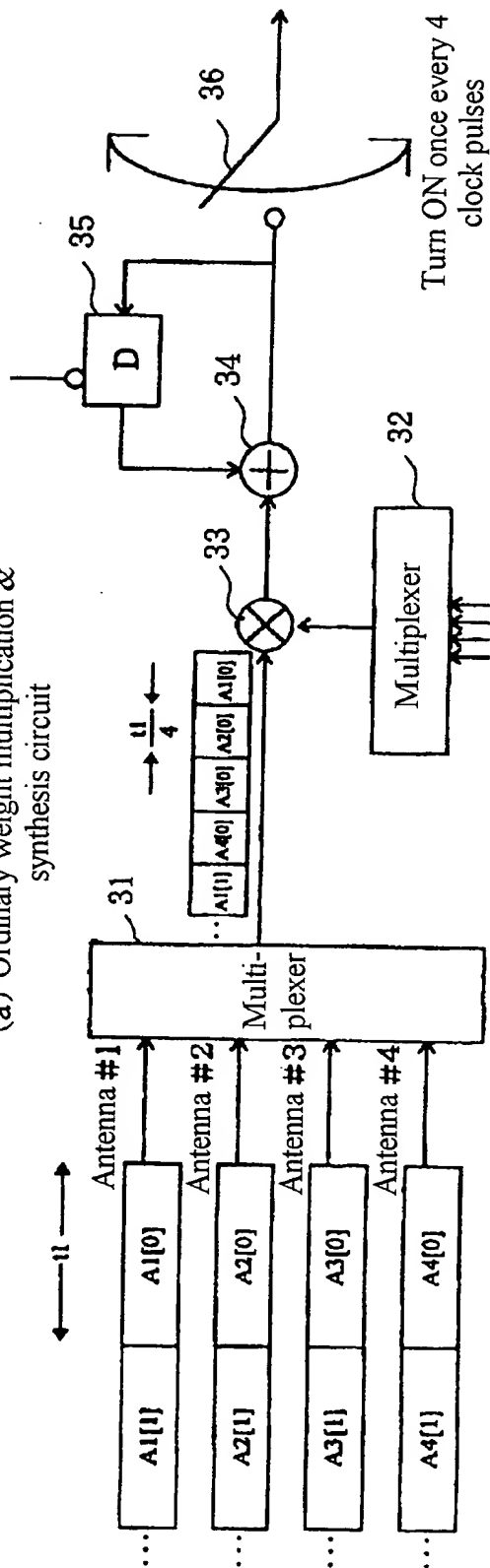
Terminate path detection if
number of paths and path levels
sufficient

Fig. 3



Clear to 0 once every 4 clock pulses

(a) Ordinary weight multiplication & synthesis circuit



Turn ON once every 4 clock pulses

Weights of individual antennas

(b) Weight multiplication & synthesis circuit using time division

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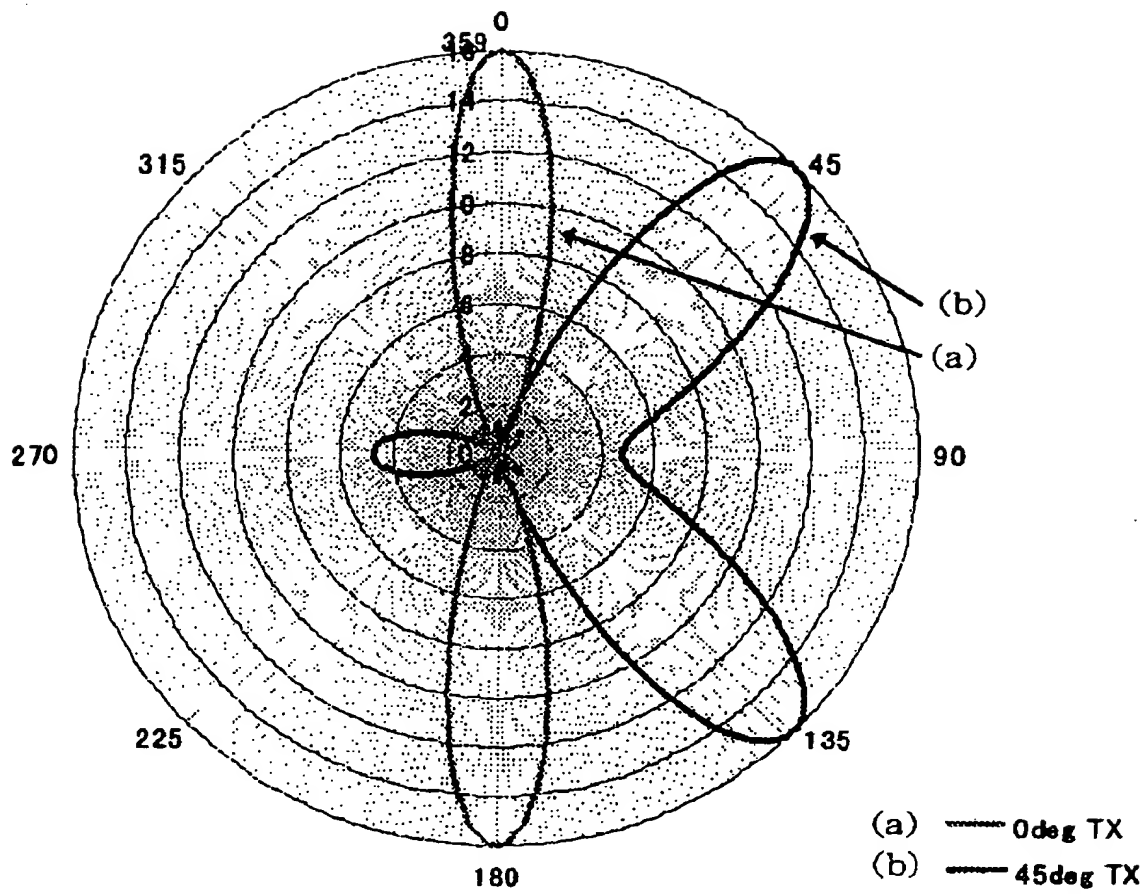


Fig. 5

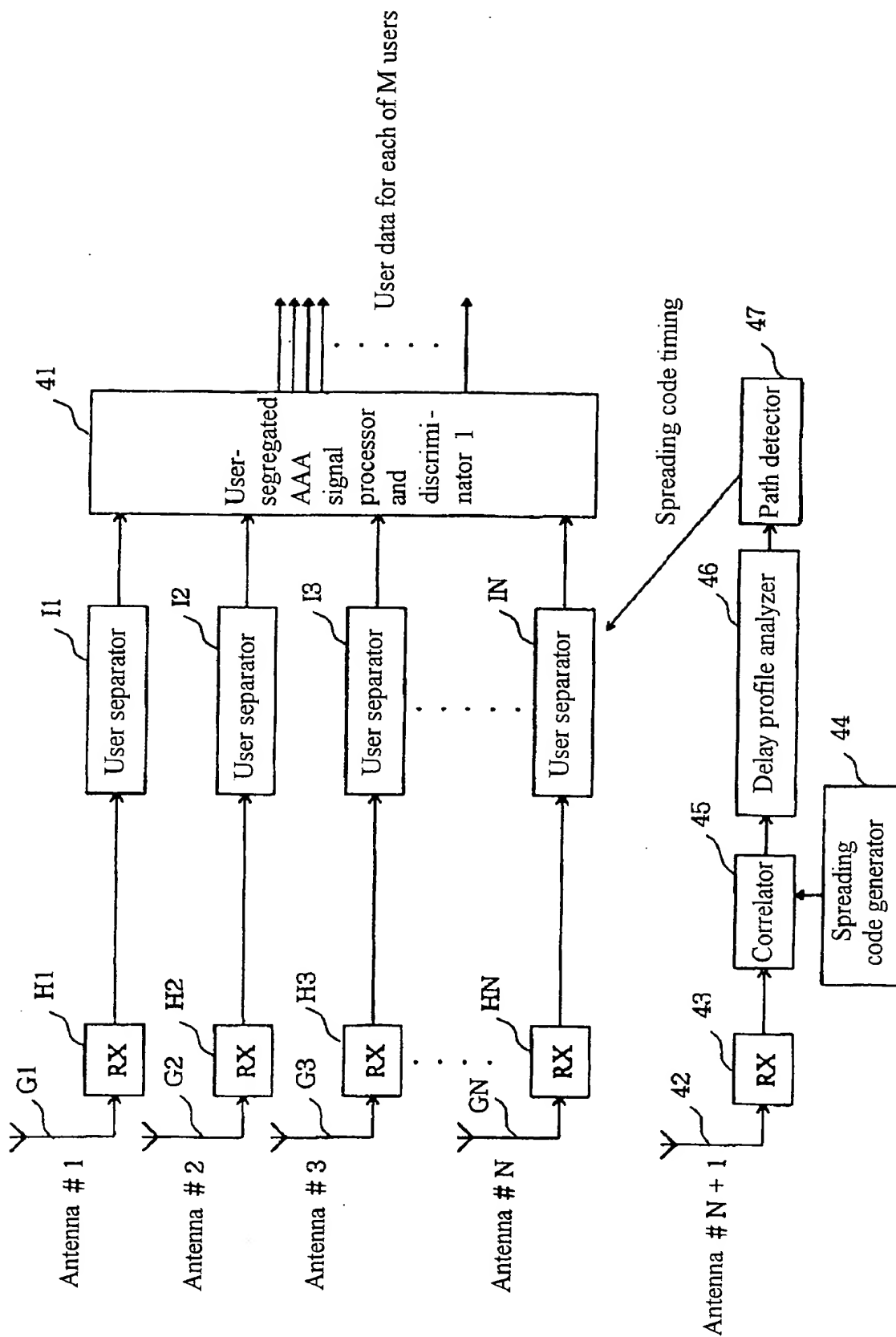


Fig. 6

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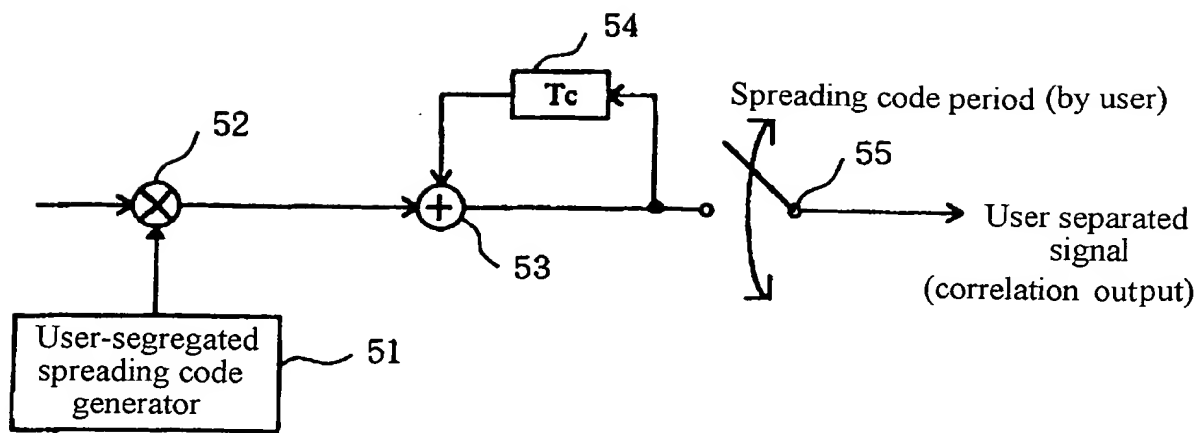


Fig. 7

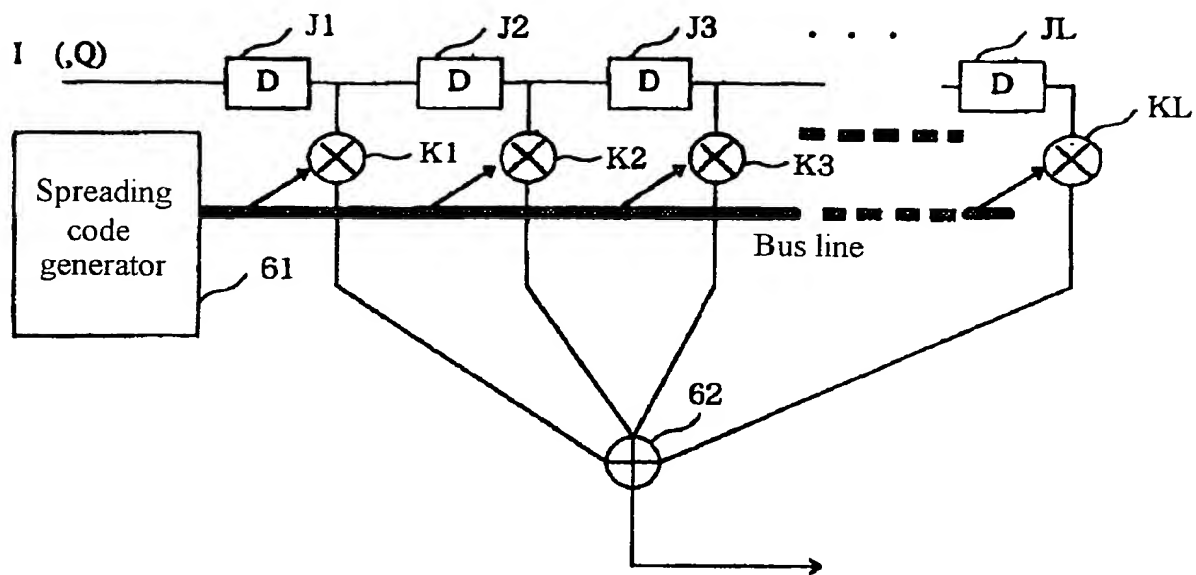


Fig. 8

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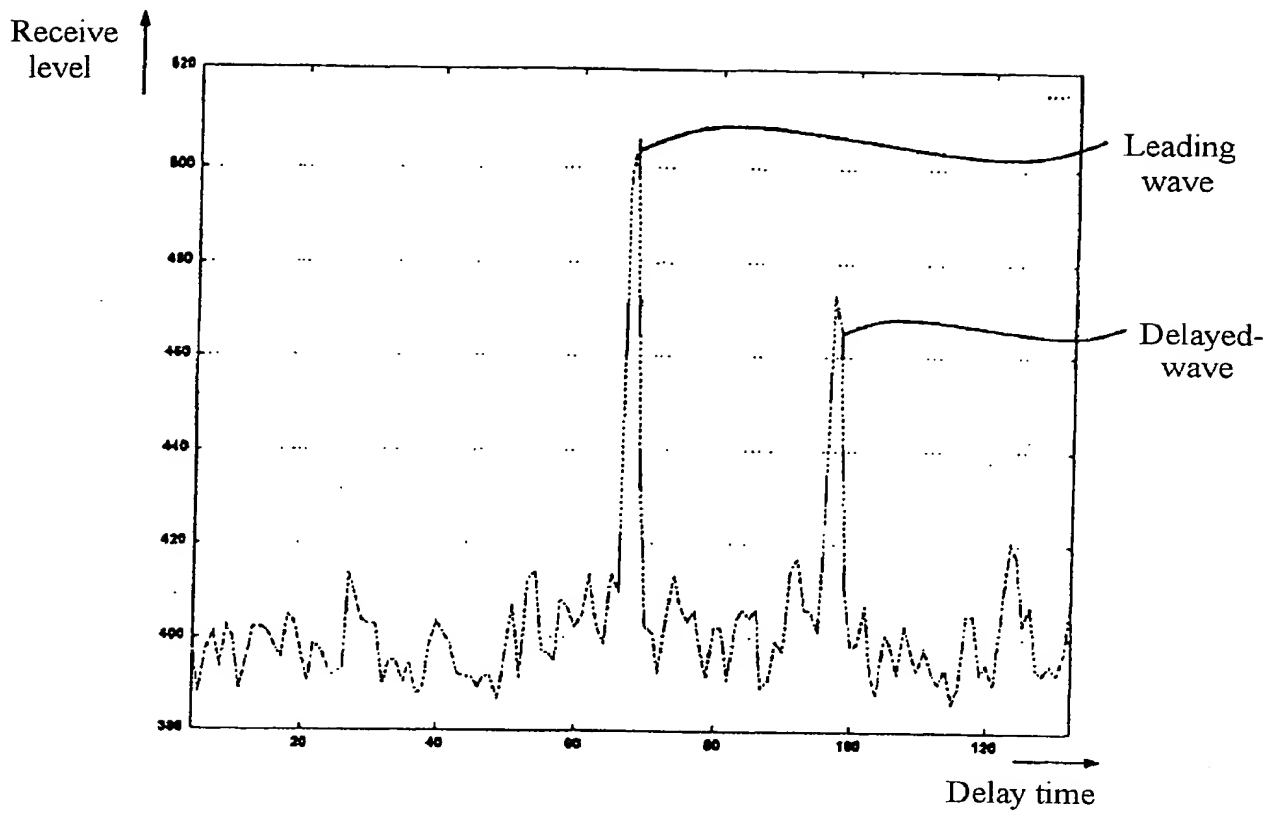


Fig. 9

T00260-28655660

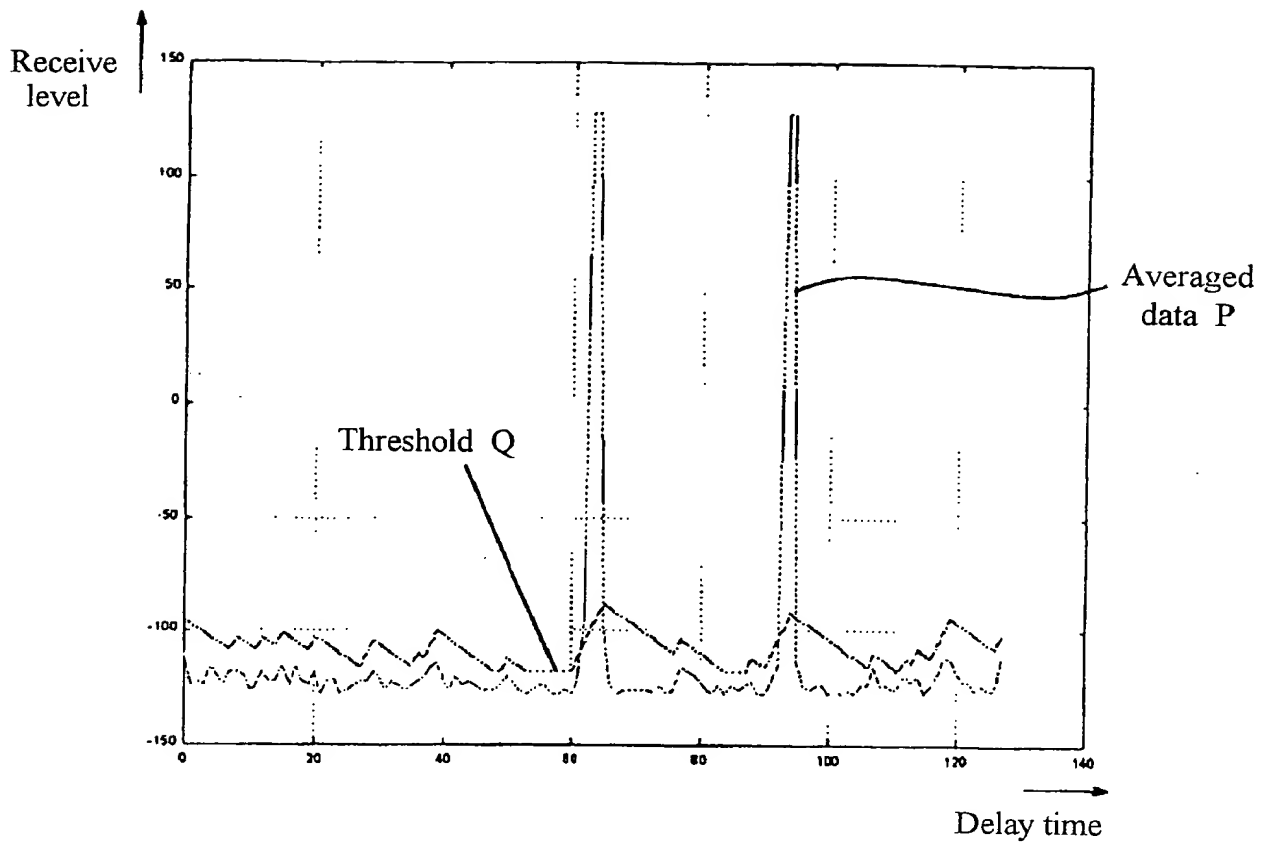


Fig. 10